

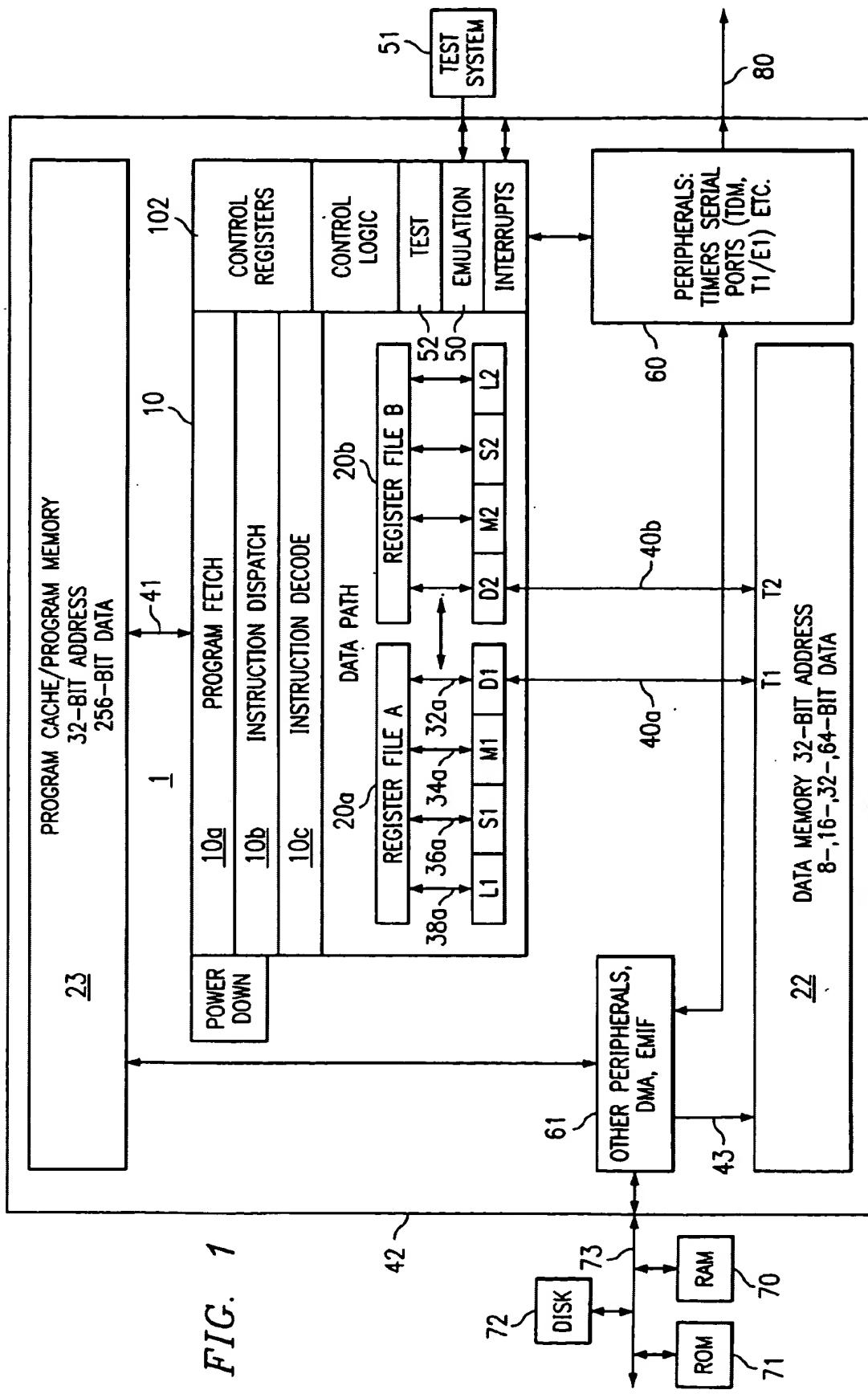
Amendment to the Drawings:

The attached annotated sheets 1, 12, and 14, and corresponding replacement sheets 1 through 10, includes changes to Figures 1, 7C, and 9. The replacement sheets formally present all Figures in this case.

In Figure 1, the block referred to by reference numeral 82, and reference numerals 82, 90, and 100 are canceled.

In Figure 7C, the multiplexer inputs "A", "B", and "C" are canceled from the Figure.

In Figure 9, reference numeral 10/40 is replaced with reference numeral 40.



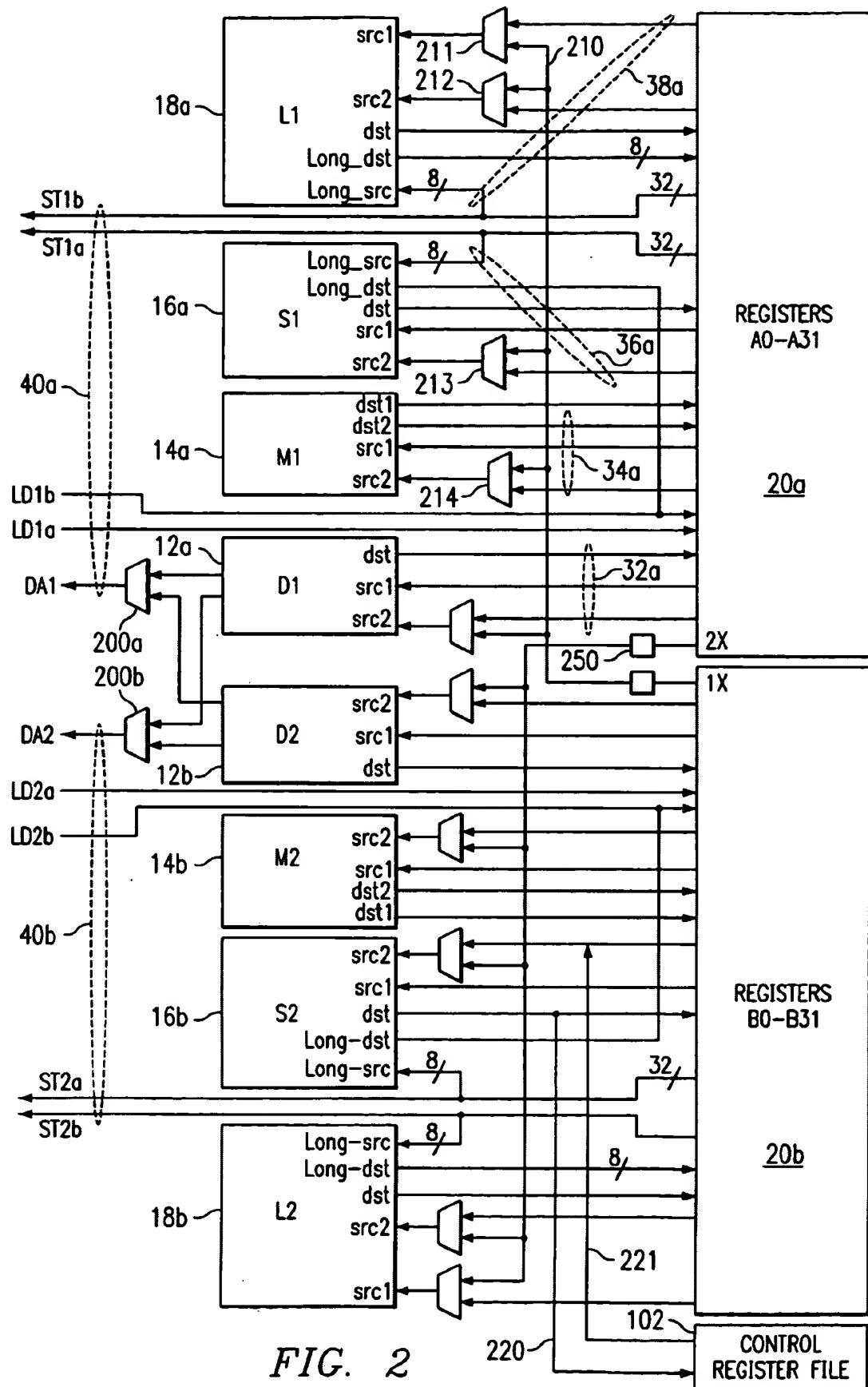


FIG. 2

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CONTROL
REGISTER FILE

REPLACEMENT SHEET

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OPERATIONS ON THE .L UNIT

31	29	28	27	23	22	18	17	13	12	11	0P	5	4	3	2	1	0
CREG	Z	DST		SCR2		SCR1/CST	X					1	1	0	S	P	FIG. 3A

OPERATIONS ON THE .M UNIT

31	29	28	27	23	22	18	17	13	12	11	0P	7	6	5	4	3	2	1	0
CREG	Z	DST		SCR2		SCR1/CST	X					0	0	0	0	S	P	FIG. 3B	

OPERATIONS ON THE .M UNIT

31	29	28	27	23	22	18	17	13	12	11	0P	5	5	5	5	5	5	5
CREG	Z	DST		SCR2		SCR1/CST	X					0	0	0	0	S	P	FIG. 3C

OPERATIONS ON THE .D UNIT

31	29	28	27	23	22	18	17	13	12	11	0P	7	6	5	4	3	2	1	0
CREG	Z	DST		SCR2		SCR1/CST	X					1	0	0	0	S	P	FIG. 3D	

LOAD/STORE WITH 15-BIT OFFSET (ON THE .D UNIT)

31	29	28	27	23	22	18	17	13	12	11	0P	8	7	6	4	3	2	1	0
CREG	Z	DST/SRC			UCST15			Y	LD/ST	1	1	S	P	FIG. 3E					

LOAD/STORE 'BASER' + 'OFFSETR/CST' ON THE .D UNIT

31	29	28	27	23	22	18	17	13	12	9	8	7	6	4	3	2	1	0
CREG	Z	DST/SRC		BASE R	OFFSET R/UCST5	MODE	R	Y	LD/ST	0	1	S	P	FIG. 3E				

REPLACEMENT SHEET

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OPERATIONS ON THE .S UNIT						
31	29	28	27	23	22	18
CREG	Z	DST	SRC2	SRC1/CST	X	OP
3	5	5	5	5	6	0

ADDK ON THE .S UNIT						
31	29	28	27	23	22	16
CREG	Z	DST	SRC2	CST	CST	0
3	5	5	5	5	5	0

BITFIELD OPERATIONS (IMMEDIATE FORMS) ON THE .S UNIT						
31	29	28	27	23	22	18
CREG	Z	DST	SRC2	CSTA	CSTB	OP
3	5	5	5	5	5	2

MK AND MKH ON THE .S UNIT						
31	29	28	27	23	22	16
CREG	Z	DST	SRC2	DST	DST	0
3	5	5	5	5	5	0

BCOND DISP ON THE .S UNIT						
31	29	28	27	23	22	10
CREG	Z	DST	SRC2	CST	CST	0
3	5	5	5	5	5	0

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REPLACEMENT SHEET

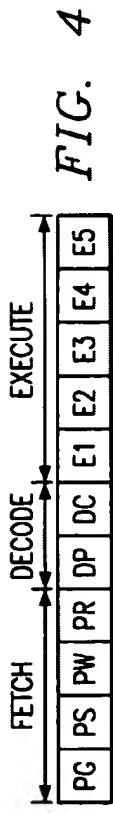
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FIG. 4

OFFSET	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
NAME	CREG	Z		DST		SRC2		SRC1	X							OPFIELD	X	1	1	0	S	P											
WIDTH	506	508	504	502	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5		

FIG. 5A

OFFSET	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
NAME	CREG	Z		DST		SRC		SRC1	X							OPFIELD	X	0	0	1	0	1	0	1	0	1	0	1	0	S	P	
WIDTH	506	508	504	502	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5	501	5	

FIG. 5B

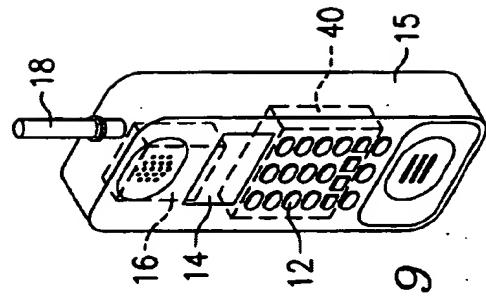


FIG. 9

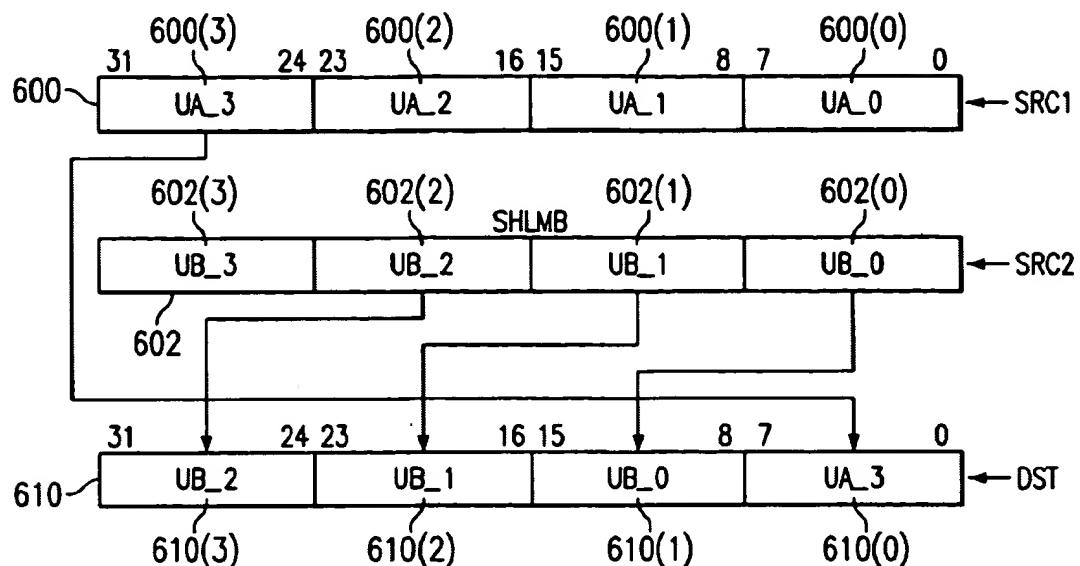


FIG. 6A

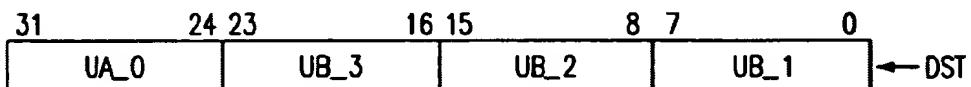


FIG. 6B

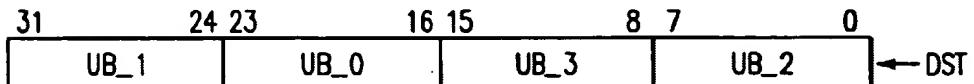


FIG. 6C

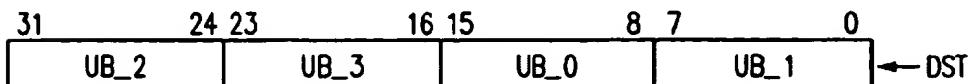


FIG. 6D

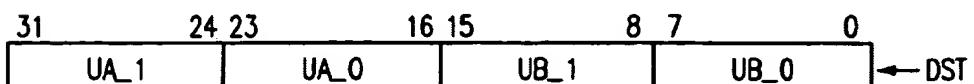


FIG. 6E

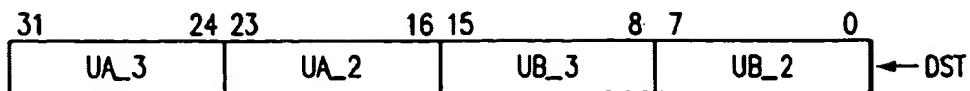


FIG. 6F

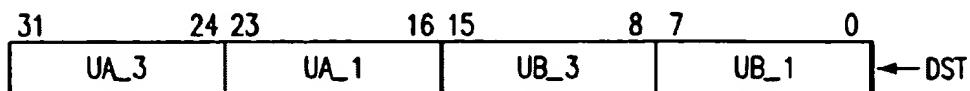


FIG. 6G

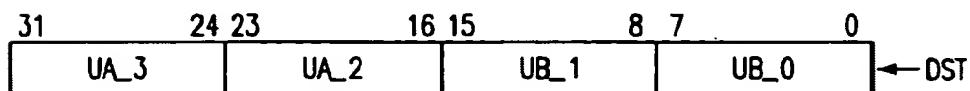


FIG. 6H

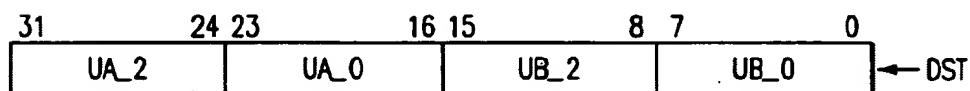


FIG. 6I

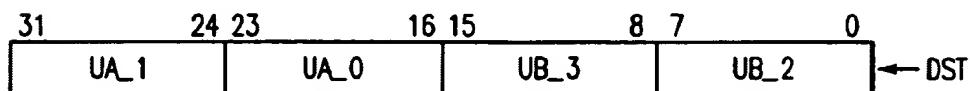


FIG. 6J

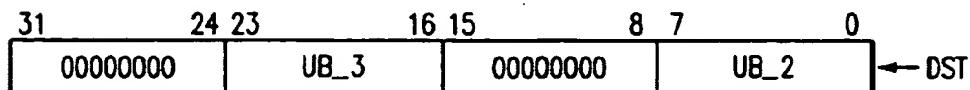


FIG. 6K

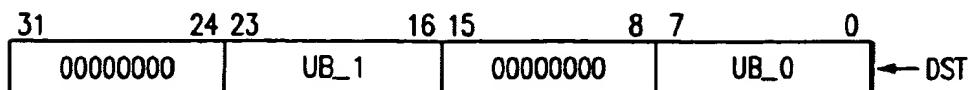


FIG. 6L

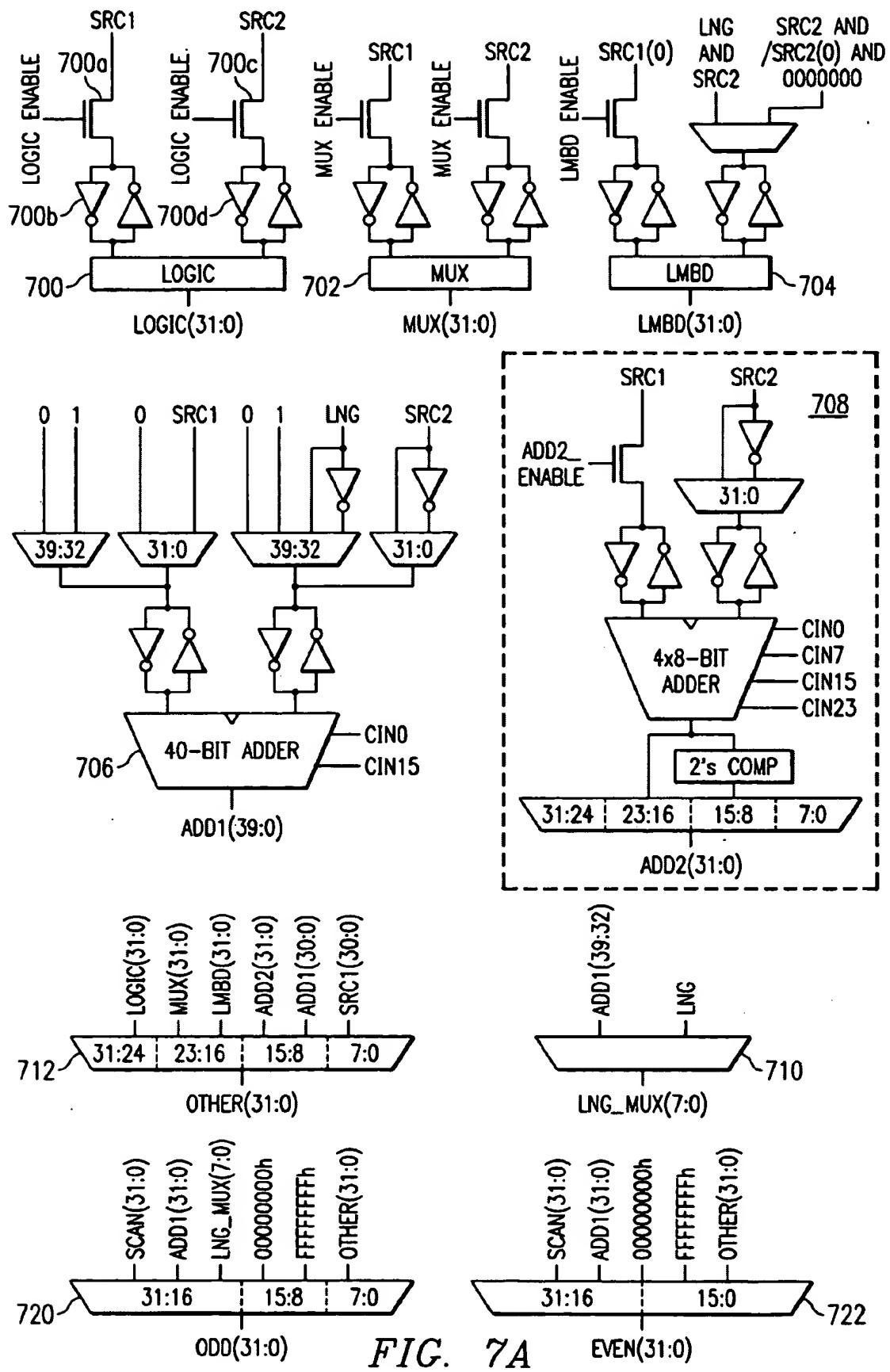
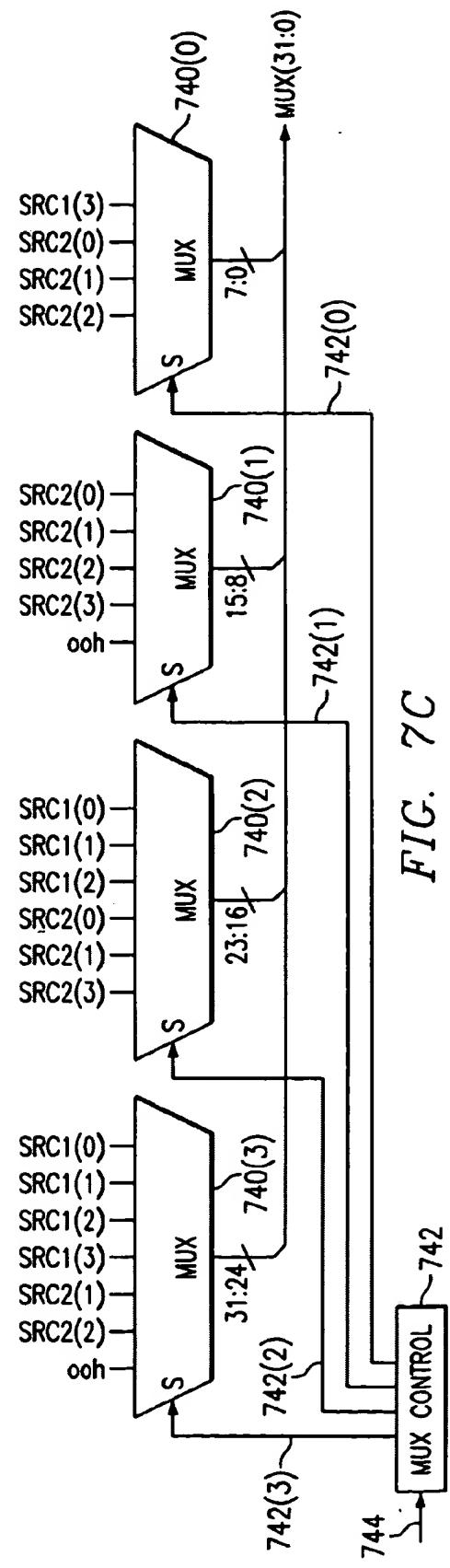
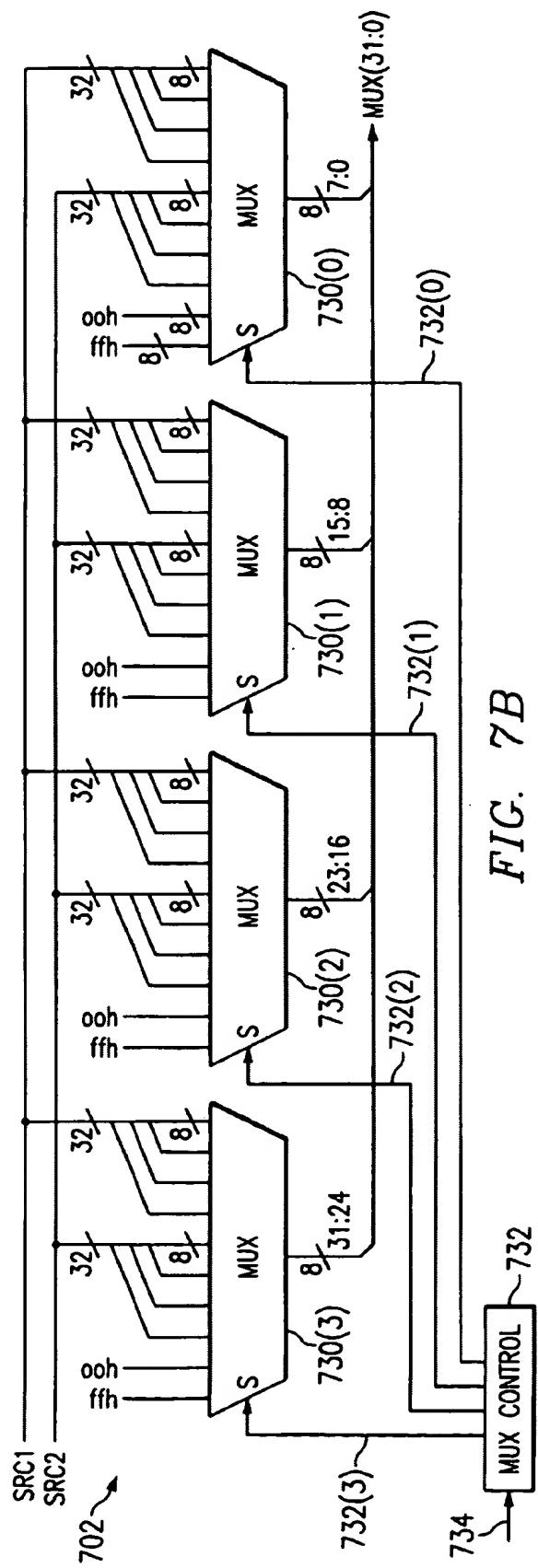


FIG. 7A



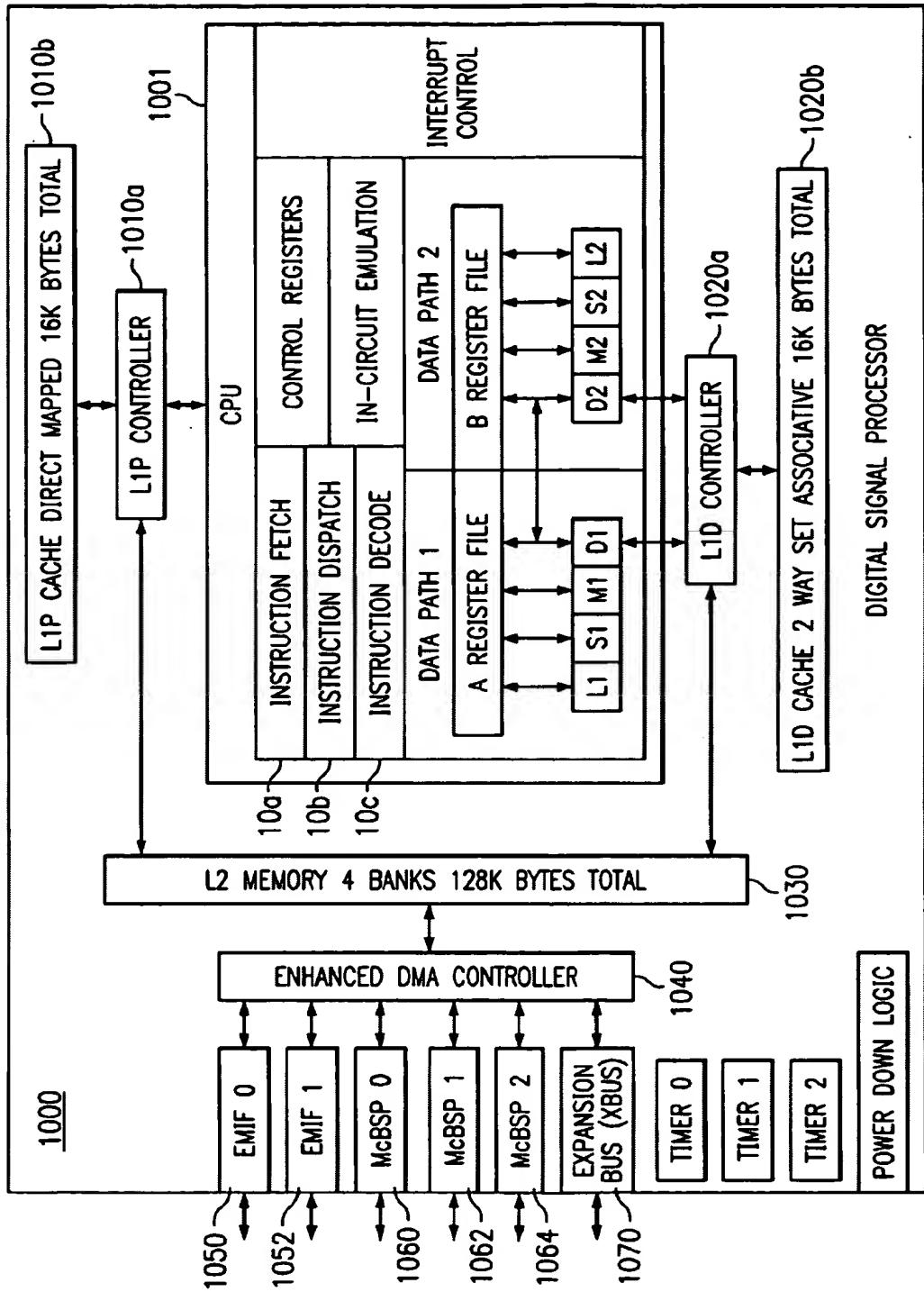


FIG. 8